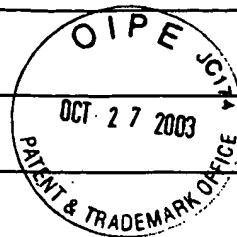


FORM PTO 1449 (modified)

U.S. DEPARTMENT OF COMMERCE
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(Use several sheets if necessary)

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Kazuhiro NOBORI et al.FILING DATE
November 28, 2000GROUP
2822

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
RR	AA	5,559,374	9-1996	Ohta et al.			
	AB	4,849,803	7-1989	Yamamoto et al.			
	AC	4,514,587	4-1985	Van Dk Soerewyn			
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	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	AJ						
	AK						
	AL						
	AM						
	AN						

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

RR	AO	Von Reinhold Bayerer et al., "Leistungshalbleitermodule in Direkty-Bonding-Technik", Technische Rundschau, Vol. 1, No. 32, August 5, 1988, pages 38-41, 43 and 45.
RR	AP	Bruce M. Romenesko, "Ball Grid Array and Flip Chip Technologies: Their Histories and Prospects", The International Journal of Microcircuits and Electronic Packaging, Vol. 19, No. 1, 1996, pages 64-74.
	AQ	

EXAMINER

DATE CONSIDERED

22 Jan 04